

**EMBEDDED ELECTRONICS**

Device modeling and digital circuit architectures	4 credits	55h
Analog electronic system architecture	4 credits	54h
Systems analysis complex	4 credits	50h
Digital signal acquisition architectures and computed controlled systems	4 credits	60h
Hardware	4 credits	55h
QSE APS 4A GEI	4 credits	47h
Communicating within organizations	6 credits	75h

# Device modeling and digital circuit architectures

 **ECTS**  
4 credits **Component**  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE **Number of  
hours**  
55h

## Presentation

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### Description

Programme (detailed contents):

The next concepts will be taught in detail during courses and laboratory work:

- the electronic phenomena in semiconductor devices and circuits
- the structure of MOS,CMOS and logical gates
- the modeling of transistors (MOS, bipolaire) and their environment sensitive parameters
- the CMOS implementation of logical functions. The high-speed, low power and gate clocking issues for CMOS gates will be presented
- the VHDL language and model for digital systems
- the synthesis and FPGA implementation of digital architectures
- the performance optimization, especially in power consumption and frequency for digital architectures

Organisation:

These concepts will be first presented during courses. Supervised exercises are then proposed on these topics. Laboratory classes are proposed to illustrate the main concepts. A part of laboratory classes are organized in a project on the design and performance optimization of digital systems. In this part the students will use a real world specification of a digital system (Ethernet controller, microprocessor).

Lectures slides, exercises and laboratory specifications will be given to students.

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### Objectives

At the end of this module, the student will have understood and be able to explain (main concepts):

- the electronic device and digital electronic systems modelling
- the issue of the integration of electronic circuits
- the design and performance optimisation of digital architectures

The student will be able to understand the models of main electronics active devices and digital complex architectures. The student will be prepared for the future technological breakout in their professional life.

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### Pre-requisites

Electrical circuits, electrostatics, analog and digital electronics,  
digital hardware.

## Useful info

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### Place

➤ Toulouse

# Analog electronic system architecture

 **ECTS**  
4 credits **Component**  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE **Number of  
hours**  
54h

## Presentation

### Description

Programme (detailed contents):

Lecture is divided in three main parts :

\* **Active filtering and modeling of noise sources:**

ü Identify sources of noise and maximize signal to noise ratio on each floor of a chain of analog signal processing.

ü Build filters from the specification to the electronic choice with the constraints and drift components.

ü Transcribe a transfer function of biquadratic filter functions and the decline in active analog filter architecture based on feedback montages (Sallen Key, Rauch, UAF, ...) or synthesis of switched capacitor filter.

ü Optimize the order of a filter based on criteria of cost, integration, stability and sensitivity.

\* **Architectures for analog information transmission :**

ü In many electronic systems, such as telecommunication or measurement systems, oscillations play an essential role

in the information processing. Each electronic system poses different requirements on these oscillations, depending on the type and performance level of that specific system. It is the designer's challenge to find the specifications for the desired oscillation and to implement an electronic circuit meeting these specifications. As the desired oscillations have to fulfill many requirements, the design process can become very complex. To find an optimal solution, the designer requires a design methodology that is preferably completely top-down oriented. To achieve such a methodology, it must be assured that each property of the system can be optimized independently of all other properties. Oscillators and Oscillator Systems: Classification, Analysis and Synthesis takes a systematic approach to the design of high-performance filters and oscillator systems. A fundamental classification of oscillators, based on their internal timing references, forms the basis of this approach.

\* **Establish a system prototype design of programmable analog circuits such FPAA.**

### Objectives

At the end of this module, the student will have understood and be able to explain (main concepts):

\* Dimension and design of analog electronic functions of information processing (filtering, amplification, automatic gain control, voltage controlled oscillators, modulators / demodulators AM and FM;

- \* Optimize the signal to noise ratio in each subset of an embedded system
- \* Modeling architectures for robust usage constraints (consumption, temperature to dissipate), the thermal variations of the environment and dispersions characteristics of components

## Useful info

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### Place

➤ Toulouse

# Systems analysis complex



ECTS  
4 credits



Component  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE



Number of  
hours  
50h

## Presentation

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### Description

This UF is composed of two distinct and autonomous parts:

- \* Multivariable systems
- \* Nonlinear systems analysis

Organisation:

Semestre 1

This two courses proceed in parallel without any particular interaction. Some exercises are performed on computer using Matlab.

### Objectives

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At the end of this module, the student will have understood and be able to explain (main concepts):

- \* Principles, difficulties and limits of the modeling of systems with multiple inputs and multiple outputs.
- \* Design and implementation of control of systems with multiple inputs and multiple outputs.

- \* Main possible and observable behaviors which can occur in the nonlinear systems (equilibrium states, limit cycles, complex behaviors) and their evolution by variation of the parameters.
- \* Basis of the theory of Lyapunov

The student will be able to:

- \* To apprehend the implementation of the control of a process with multiple inputs and multiple outputs.
- \* To begin the analysis of a nonlinear system by various techniques (qualitative, geometrical, and simulations)
- \* To lean on numerical analysis (Matlab©) to establish, confirm, validate, simulate and implement the theoretical results discussed during the courses.

## Useful info

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### Place

➤ Toulouse

# Digital signal acquisition architectures and computed controlled systems



ECTS  
4 credits



Component  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE



Number of  
hours  
60h

## Presentation

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### Description

Programme (detailed contents):

Lecture is divided in three complementary axes:

- \* **Electronics design:** different techniques used in analog and digital converters, with their associated architectures are detailed. Companding techniques are presented (A-law). The complete chain gauging techniques are studied based on the signal noise ratio extraction.
- \* **Command design:** discrete time linear models: input/output model, state space models. Sampling with zero holder. Stability criteria. How to obtain a discrete time controller from a continuous time one. RST controller: Regulation and tracking. Relation with state space methods.
- \* **Project:** the purpose is to cover several items such as treating an analogue sensor, controlling an A/N converter, the necessary digital processing (minimal control law) and finally the N/A conversion to drive the actuator. The whole chain will be operated by a microcontroller.

### Objectives

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At the end of this module, the student will have understood and be able to explain (main concepts):

The complete modelling from sensor to actuator is presented, associated with digital control technics. A specific labwork deal with the implementation of a complete chain of acquisition and digital processing in order to carry out the control of an actuator.

## Useful info

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### Place

➤ Toulouse



# Hardware



ECTS  
4 credits



Component  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE



Number of  
hours  
55h

## Presentation

The 2 parts are held in parallel and are the subject of distinct evaluations

### Description

Programme (detailed contents):

- \* System Design part:

The lecture begins with a presentation of the object paradigm. In this part, many various examples are given to illustrate the object world: systems of services or software and hardware systems. The second part deals with the specificity of an object-oriented design process. This process is compared to a functional approach. The lecture moves on to an UML introduction. The last part details the most widely used UML diagrams. It starts with use case and sequence diagrams, then structure (class and composite structure) diagrams are studied. Finally, behaviour (state and activity) diagrams are presented. Students assess their knowledge with exercises and labworks during which they have to model several complex systems.

- \* Programming of microcontroller part :

The functions and the programming of the STM32 (based on a ARM/CortexM3 core) is studied essentially in labwork. Half of time is devoted to a project. This project consists with the software development of an embedded system embarked.

Organisation:

### Objectives

At the end of this module, the student will have understood and be able to explain (main concepts):

- \* Programming of microcontroller part :
  - \* Programming specificities of the peripheral units for microcontroller.
  - \* How to take into account hardware constraints for the design of embedded system.
- \* System Design part:
  - \* o the concepts of object paradigm.
  - \* o the concepts of object-oriented design process
  - \* o the main UML diagrams involved in an object modelling: use case diagram, sequence diagram, state and activity diagram and composite structure diagram.

The student will be able to:

- \* Programming of microcontroller part :
  - \* To select an architecture processor adapted to the software application and to the process configuration.
  - \* To conceive and test the techniques of the programming by hardware interruption.
  - \* To use debug tools and test in the context of cross-development.
  - \* To find information into datasheet.
- \* System Design part:
  - \* o breakdown a system (which could be software or hardware) with an object-oriented approach
  - \* o choose the diagrams which are most appropriated according the student viewpoint: structure, behaviour, interaction

propose an object-oriented UML model of a software or hardware system

## Useful info

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### Place

> Toulouse

## QSE APS 4A GEI

 ECTS  
4 credits

 Component  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE

 Number of  
hours  
47h


## Useful info

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### Place

➤ Toulouse

# Communicating within organizations

 **ECTS**  
6 credits **Component**  
INSTITUT  
NATIONAL  
DES SCIENCES  
APPLIQUEES  
TOULOUSE **Number of  
hours**  
75h

## Presentation

<https://moodle.insa-toulouse.fr/course/view.php?id=44>

## Objectives

The classes given in French will focus on :

- How to react to society's demand for technical and scientific information
- How to foster critical thinking in order to give appropriate answers when questioned about such issues
- How to communicate effectively in the workplace

The classes given in English will focus on the specific linguistic characteristics of English used in such contexts in order for the students to understand and master them.

The students will also be made aware of the specificity of professional communication within the English-speaking world

Module L2

The objectives, defined in reference to the CEFR for the 5 language activities, depend on the language studied - Chinese, German, Spanish - and the level of the student.

They can be consulted on :

In certain cases, students may be authorised to follow an English module instead of another language.

## Pre-requisites

For classes in English : mastery of general English.

## Useful info

### Place

➤ Toulouse