

Systems on chip



ECTS
4 credits



Component
INSTITUT
NATIONAL
DES SCIENCES
APPLIQUEES
TOULOUSE



Number of
hours
50h

Presentation

Objectives

At the end of this module, the student will have understood and be able to explain (main concepts):-the design and optimisation of advanced digital systems-the life cycle of a software-hardware system (specifications, design, implementation)-co-design of software hardware complex systems upon the application requests-co-verification of new software & hardware complex systemsThe student will be able to:-design and implement advanced digital systems on FPGA using VHDL and optimise their performances in power consumption and functioning frequency upon the application requests-design and implement hardware and software systems on programmable chips (SoPC) and systems on chip (SoC)

Pre-requisites

Computer engineering

Exigencies engineering

Useful info

Place

> Toulouse